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Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. UTILITY Patent Application

Q.I.P.E.

PATENT DATE

SCANNED

SCANNED 1028A

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/884675		714	701	2-184	EDD

APPlicants

Matthew Reilly
Matthew Mattina
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TITLE

Hardware efficient handling of instruction exceptions to limit adverse impact on performance

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. _____ _____ _____	<div style="text-align: center;"> (Assistant Examiner) (Date) </div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. _____ _____ _____	<div style="text-align: center;"> (Primary Examiner) (Date) </div>			Amount Due	Date Paid
				ISSUE BATCH NUMBER	
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